

QUAD FLAT NO-LEAD (QFN) GRID ARRAY PACKAGE,
METHOD OF MAKING AND MEMORY MODULE AND
COMPUTER SYSTEM INCLUDING SAME

Inventors: Setho Sing Fee et al.

Filed: December 5, 2003

Attorney Docket No. 4738.2US

1/5

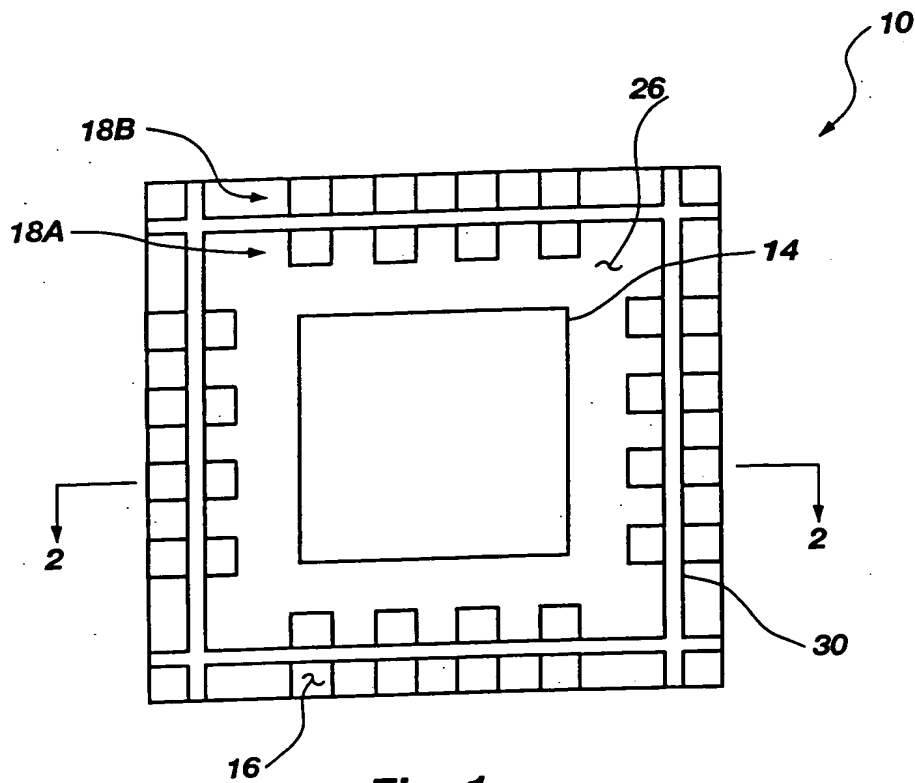


Fig. 1

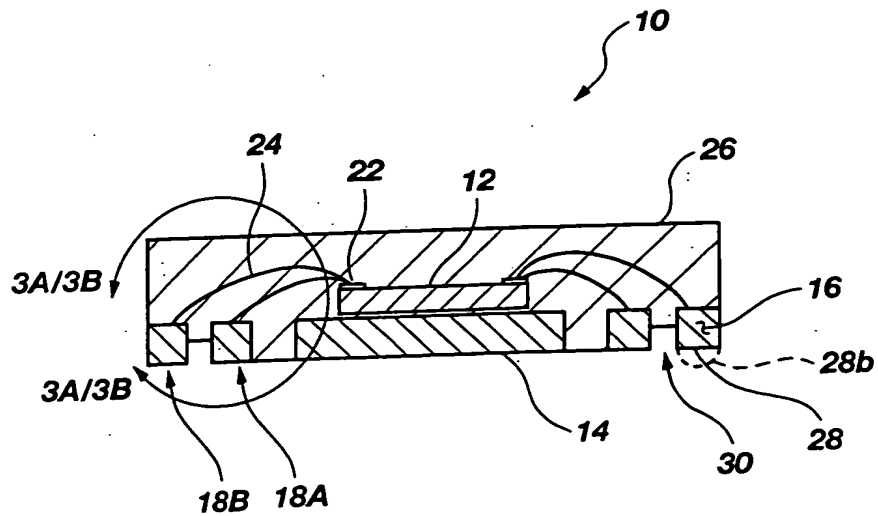


Fig. 2

QUAD FLAT NO-LEAD (QFN) GRID ARRAY PACKAGE,
METHOD OF MAKING AND MEMORY MODULE AND
COMPUTER SYSTEM INCLUDING SAME

Inventors: Setho Sing Fee et al.

Filed: December 5, 2003

Attorney Docket No. 4738.2US

2/5

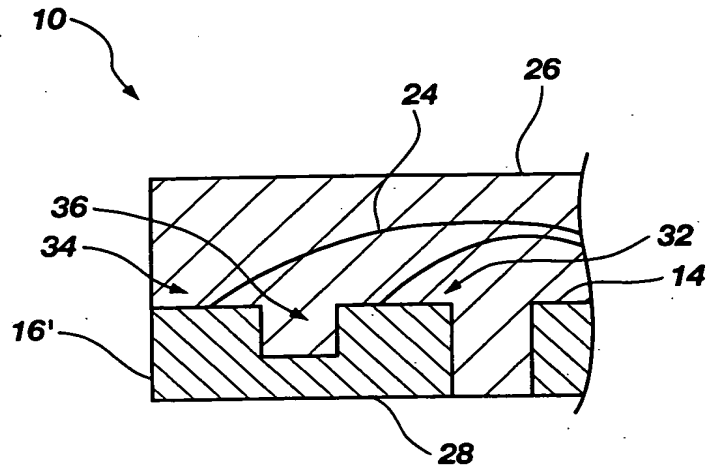


Fig. 3A

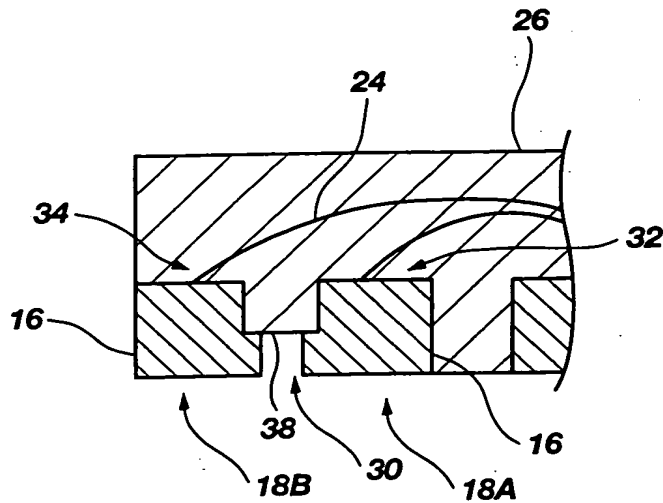


Fig. 3B

QUAD FLAT NO-LEAD (QFN) GRID ARRAY PACKAGE,
METHOD OF MAKING AND MEMORY MODULE AND
COMPUTER SYSTEM INCLUDING SAME

Inventors: Setho Sing Fee et al.

Filed: December 5, 2003

Attorney Docket No. 4738.2US

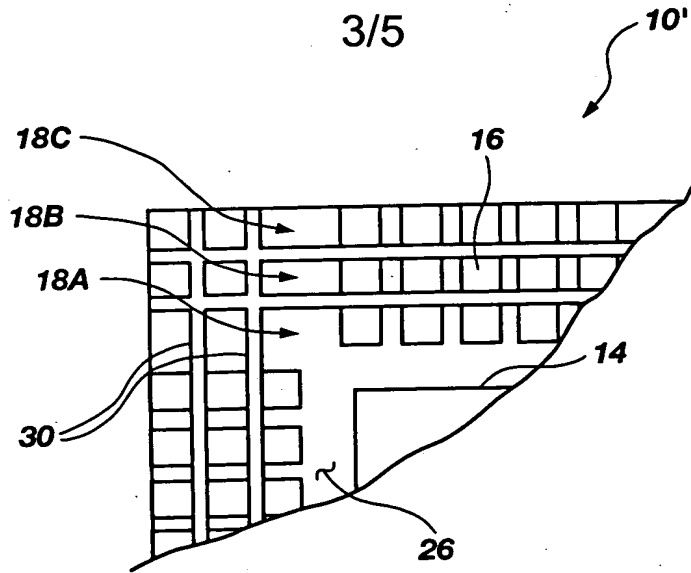


Fig. 4A

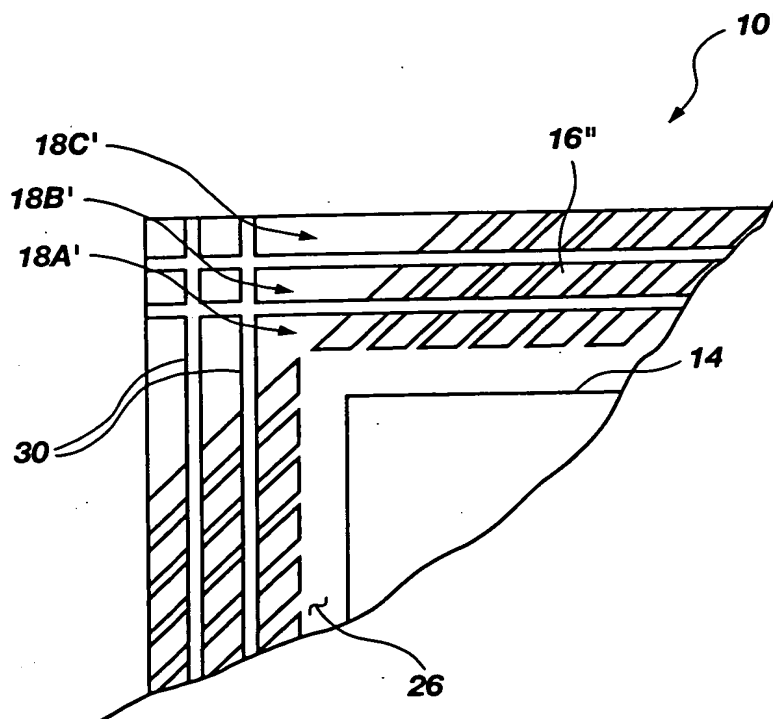


Fig. 4B

Inventors: Setho Sing Fee et al.
Filed: December 5, 2003
Attorney Docket No. 4738.2US

Filed: December 5, 2003

Filed: December 9, 2009
Attorney Docket No. 4738.2US



5/5

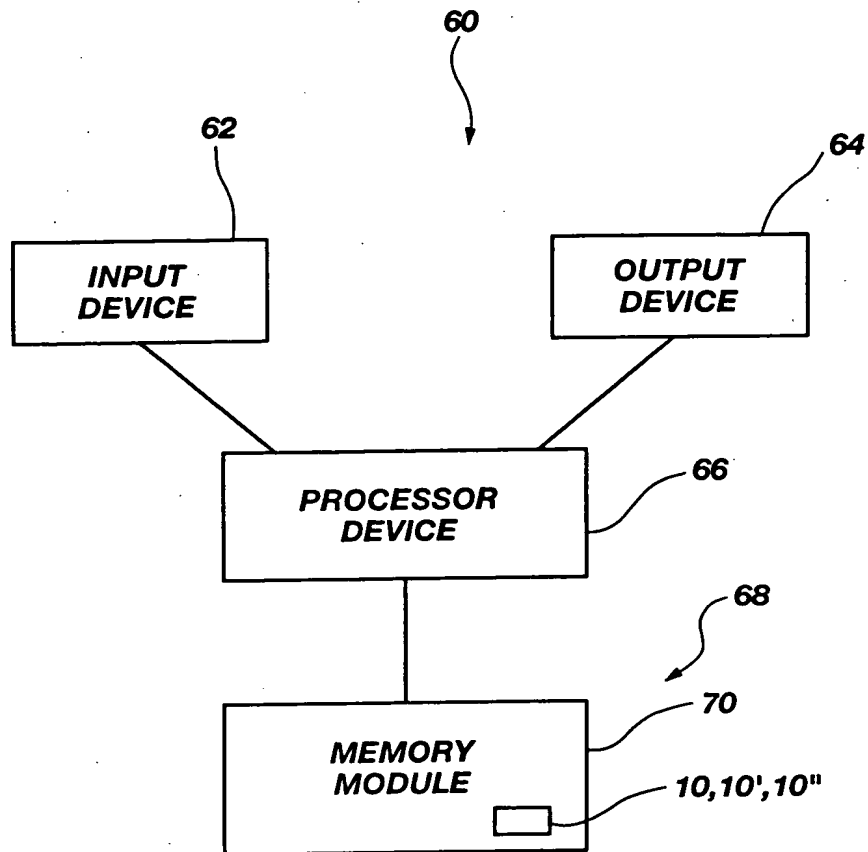


Fig. 6